

Amendments to Specification:

Please replace the paragraph beginning at page 19, Line 14 with the following paragraph:

Referring now to Figure 4, a network system 410 suitable for implementing the present invention includes a first route processor 467, a second router processor 469, a master central processing unit (CPU) 62 462, interfaces 468, and a bus 415 (e.g., a PCI bus). When acting under the control of appropriate software or firmware, the CPU 462 is responsible for network management. Each of the route processors 467 and 469 may also include a processor and memory (not shown) that are together responsible for implementing various routing tasks, HSRP techniques, and the recovery techniques of the present invention. The route processors and CPU 462 preferably accomplish all these functions under the control of software including an operating system (e.g., the Internetwork Operating System (IOS®) of Cisco Systems, Inc.) and any appropriate applications software. CPU 462 and the route processors may each include one or more processors (e.g., 463), such as a processor from the Motorola family of microprocessors or the MIPS family of microprocessors. In an alternative embodiment, processor 463 is specially designed hardware for controlling the operations of network system 410. In a specific embodiment, a memory 461 (such as non-volatile RAM and/or ROM) also forms part of CPU 462. However, there are many different ways in which memory could be coupled to the system. Memory block 461 may be used for a variety of purposes such as, for example, caching and/or storing data, programming instructions, etc.